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CCD X-ray detectors: opportunities and challenges

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Charge coupled device (CCD) arrays are showing considerable promise for a variety of X-ray imaging applications. Performance characteristics such as high dynamic range, versatile readout and data-storage modes, and time-resolved capabilities, present interesting opportunities for new kinds of experiments on high-brilliance sources. We illustrate some of these possibilities with results of our recent synchrotron radiation studies using CCDs as high-resolution position-sensitive detectors. While important hurdles have already been overcome, several challenges remain for the effective exploitation of CCDs in X-ray applications. Among these are requirements for further increases in spatial resolution, radiation hardening, and very-high-speed data acquisition. Ongoing developments and future directions are discussed in this context.

1. Introduction

Charge coupled devices (CCDs) are currently the pre-eminent choice for a wide range of optical imaging applications [1], ranging from video cameras to highsensitivity imagers for astronomical telescopes. In recent years the utility of CCDs as X-ray detectors has gained increasing recognition and this technology is emerging as a major approach to new generations of X-ray detectors for synchrotron radiation research.

With the advent of high-brilliance synchrotron sources such as ESRF and APS, X-ray detectors will need to perform at a far more demanding level than at present. They must be able to handle large fluxes (i.e. have large dynamic range), have inherently fast data handling capabilities and be capable of imaging with high spatial resolution. Some of these characteristics compete with each other so that it is hard to meet a wide range of requirements with a single detector design. However, CCD arrays are very versatile and lend themselves well to different detector performance requirements.

In this paper we summarize some of the current directions in CCD X-ray detector development in the context of synchrotron radiation research. We will illustrate the available opportunities with some of our recent results, pointing out where additional work is required to meet the challenges of third-generation synchrotron sources.

2. Experimental requirements

In general, synchrotron radiation X-ray experiments require three main components: a bright tunable source, efficient detector systems and well-designed X-ray optics. These three requirements are clearly interrelated and are each equally important. A good example is the demanding set of experiments recently carried out by our group at beamline X-16B at the National Synchrotron Light Source, NSLS, to demonstrate the feasibility of performing fast time-resolved X-ray scattering measurements.

The experimental layout for these experiments is shown in Fig. 1: a Lemonnier-type [2] asymmetric-cut Ge (111) bent crystal monochromatizes the white beam providing narrow-band ($\Delta E/E \approx 2 \times 10^{-4}$) radiation which is angularly dispersed in the horizontal plane. $\Delta\theta \approx 200$ arcsec (see Fig. 2). For the time-resolved X-ray scattering described here, the horizontal angular dispersion provides a fan of nearly monochromatic X rays incident on the sample over a range of Bragg angles. When used in conjunction with a position sensitive detector, such as a CCD pixel array, this angulardispersive method is capable of recording X-ray scattering intensities simultaneously over small portions of reciprocal space close to a Bragg peak (Fig. 3). This is accomplished without mechanically scanning either the crystal or the detector. The method is particularly well suited to measuring time-dependent strain profiles and surface / interface structures in samples which are close to the ideal crystal limit, that is to say structures with rocking curves narrower than a few hundred arcseconds [3]. This situation includes some very important classes of technological materials including semiconductor heterostructures, magnetic multilayers, and superconducting thin films.

3. CCD imaging and readout

The basic operation of a CCD array can be readily understood with reference to Fig. 4. Each pixel in the detector array acts as an MOS-type capacitor storing



Fig. 1. Experimental arrangement for angular dispersive realtime X-ray scattering studies using a CCD detector. The bent crystal is asymmetric cut Ge(111) The X-ray optics correspond to the configuration of the X-16B AT&T Bell Labs

beam line at the National Synchrotron Light Source.

photo-generated charge under a polysilicon gate electrode. 1 electron per 3.65 eV of incident photon energy is generated and the full-well depth is typically $3-5 \times 10^5$ electrons for a 20 μ m \times 20 μ m pixel. The full-well depth is proportional to the pixel area.

During readout the charge accumulated in each pixel is transferred row-by-row to the serial (output) register by a sequence of clocking pulses applied to the parallel register. Once it reaches the serial register a row of pixels is read out pixel-by-pixel through an on-chip output amplifier. After further amplification the analog output can be digitized and stored in memory. To reduce readout noise associated with the output amplifier reset function, correlated double sampling [4] (CDS) is frequently used. CDS can be implemented on the chip in some of the newer designs, or it can be performed with an external circuit.

Typically CCD chips used in scientific applications are cooled to reduce dark current generation. Typical numbers at ambient temperature range from a few hundred to a few thousand electrons per pixel per second, reducing by a factor of 2 for every $\sim 8^{\circ}$ C drop in temperature. Thus, for most applications, except for very long exposure times, thermoelectric cooling to



Fig. 3. Angular dispersive scattering geometry using a CCD array detector.

approximately -30° C is sufficient. For some of the very fast experiments referred to below, ambient temperature operation will be satisfactory.

For more details on the operation of scientific CCDs [5], their development history [6], and specific application to X-ray detection [7,8], the reader is directed to a number of excellent reviews in the literature.

4. Direct detection of X rays

Several X-ray detectors based on CCDs employ phosphors or scintillators to convert the X rays to visible photons. Frequently, fiber optic tapers are used as face plates in such detectors. For our specific applications, requiring very fast readout and moderately high spatial resolution, the use of such converters can be detrimental. Accordingly, we have focused most of our CCD work on direct detection of X rays [9].

Direct detection is made possible by the approximate matching of the absorption depth of silicon ($\sim 100 \ \mu m$ for 8 keV X rays) with the MOS-depletion-layer depth, which depends mainly on the dopant concentra-



Fig. 2 Calculated dependence of (a) energy bandpass and (b) angular divergence of synchrotron beam on the monochromator-focus distance, p'. The data points in (b) were measured on beam line X-16B at the NSLS.





tion. For most commercially-available CCDs the depletion depths are fairly small (4–15 μ m, typically). Nevertheless, such devices offer useful detector quantum efficiencies (DQEs) in the range 5–15% for X rays in the 10–20 keV range. With current manufacturing techniques it is possible to produce depletion layers as thick as 50 μ m using very low dopant concentrations (<10¹² cm⁻³). Such "deep depletion" devices have X-ray DQEs approaching 100%, albeit at the expense of decreased spatial resolution.

Most CCD chips are intended for use in front-side illumination. For X-ray photon energies in the range < 10 keV it can be advantageous to illuminate the detector from the back so that absorption by the gate oxide and electrode structure is avoided. In this mode the substrate of the device is mechanically thinned (to $\sim 10 \ \mu$ m). Several successful back-thinned devices have been reported and the process is now considered to be routine. For our purposes in the X-ray region, either back- or front-side illumination can be used. It remains to be seen which approach will be more favorable for detector applications.

5. Radiation damage

All silicon devices that are used for direct X-ray detection suffer to some degree from radiation damage. An important question is whether such damage is acceptable in terms of useful operation of the detector. First, we describe briefly what kind of effects are produced in the energy range of interest.

The radiation damage for X rays in the 5-20 keV region manifests itself as traps in the oxide layer. The main effect is to produce an increase in the dark current (see Fig. 5). However, from this data [10] we see that up to 0.5 Mrad dose has to be absorbed before the additional dark current exceeds the signal from one X-ray photon at 10 keV. For practical purposes, then, radiation damage has a minimal effect on a CCD detector used in this mode. The effects of this kind of radiation damage (as opposed to the lattice damage created by energetic particles and gamma-rays) can be reversed by UV illumination or by a simple thermal annealing process [11]. We have not observed any significant changes in the charge transfer efficiency (CTE) for the CCDs we have evaluated.

Our conclusion is that radiation damage for 5–20 keV X rays is not a serious limitation for all but the most demanding applications (e.g., very low photon flux requiring long exposures). We have operated a detector based on a Loral MPP CCD for over 200 h in diffuse scattering experiments on an NSLS bending-magnet beam line. This exposure produced no notice-able degradation of the CCD chip at the level of 12-bit resolution.



Fig. 5. Excess dark current generated by X-ray radiation damage in Loral MPP CCD. The "inverted" mode refers to an operating condition in which one or more of the array clocks are negatively biased sufficient to pin the $Si-SiO_2$ interface potential to the substrate potential. This reduces overall dark current but also decreases the full well capacity. Data taken on a rotating anode source with 8 keV X rays [10].



Fig. 6. CCD used in "streak mode" to record time-resolved X-ray scattering data on the kinetics of annealing of epitaxial Ge on GaAs. Time (increasing temperature) is to the left. The large ridge of intensity at around 115 pixels corresponds to the substrate and the smaller ridge in front of it corresponds to the Ge overlayer. The splitting between the two peaks is approximately 300 arcsec at the commencement of annealing. The sample was grown at a substrate temperature of 350°C

and the maximum annealing temperature was 550°C.

One reason for the relative radiation hardness of the MPP [12] chip relates to the use of shallow ion-implants in the gate electrode structure which "pin" the subelectrode potentials and help to stabilize the charge transfer process. Our earlier experiments [13] performed with a "virtual phase" CCD (TI 4849) [14] showed that this deeply-implanted device had an even greater tolerance to radiation damage. However, the deep implants in that case have detrimental side effects; for example, the CTE was inherently poorer than that of the MPP design.

6. Results using direct X-ray detection

Here we illustrate the usefulness of direct detection of X rays by CCDs with an example drawn from our recent studies on beam line X16-B at NSLS. This example emphasizes both the high spatial resolution inherent in CCDs as well as the potential for very fast time-resolved operation.

In Fig. 6 we show a readout from a Loral 516×516 CCD which has been used at a 1 MHz readout rate to record in real time the diffraction pattern from an epitaxial layer of Ge grown on GaAs [15]. This is particularly challenging because Ge and GaAs have very close lattice matching ($\sim 0.2\%$) and the epilayer grows pseudomorphically. Therefore, it is very difficult to image the Ge and GaAs peaks separately with precision using standard diffraction techniques. The experimental arrangement is shown in Fig. 1.

In order to obtain real-time information the one-dimensional diffraction pattern is imaged on a single row of the CCD. Utilizing the parallel transfer capability of the device we can record changes in the diffraction pattern as a function of time as the pattern is electronically "streaked" downwards towards the serial output register. In this case the temperature of the sample was ramped in a rapid thermal annealing cycle to investigate the kinetics of structural changes at the epitaxial interface. The data in Fig. 6 illustrate that we could track changes in lattice parameter with a precision of ~1 in 10⁶ on a time scale of ~1 ms. The temporal resolution in this experiment was limited by photon flux, not by the speed of the detector. Row-transfer times down to a few hundred nanoseconds are feasible in some types of CCD arrays, allowing measurements to be made within a single bunch period ($\sim 3 \ \mu s$ at APS).

In the above example, the data were acquired using a custom-made VME buffer [16] and transferred to hard disc through a SCUSI (Small Computer System Interface). The data is displayed on a SUN workstation, which also handles global control of the detector system.

7. Future prospects and challenges

The time-resolved techniques described here are the result of matching efficient X-ray optics to fast parallel data acquisition methods based on CCDs. We can expect further dramatic increases in readout speed as multiple output devices are brought into operation. These are CCDs in which the active area is divided into segments, each having its own output amplifier and CDS circuitry. Fast-framing devices are now available with 16 or 32 output channels [17]. The compatibility of some multiport devices with direct X-ray detection is currently under evaluation by our group.

One of the pressing challenges for CCD X-ray detectors obviously relates to very-high-speed data acquisition and analysis. Fortunately the technology in this area continues to enjoy an exponential evolution, and it seems likely that the speed and capacity of A/Dconverters, CPU, computer buses, mass memory, etc., will keep pace with the detector community's needs. For example, our group is working on designs of parallel detector systems which will operate at rates in excess of 1 Gbit s⁻¹ by means of current technology. While it may be feasible to acquire data at such high rates, it will clearly require some thought as to how to access the results in an efficient and useful way. Thus, software development for visualization and graphical user interfaces will be extremely important. The development of smart pixel devices [18], which are selective as to the access of *useful* information, will be a significant improvement.

We have not discussed developments which are underway towards increasing the active *area* of CCD detectors. In some fields such as protein crystallography, and medical imaging, this is an important requirement. This challenge is being met by two different approaches, one using an array of fiber-optic-coupled CCDs [19] and the other using large-area image intensifiers [20].

Another important challenge is concerned with achieving still higher spatial resolution. Currently the CCD with the smallest pixel size is the Kodak KAF-1400 chip with $6.8 \times 6.8 \ \mu$ m pixels. Since there are practical considerations which limit the minimum pixel size, optical demagnification schemes will be needed to further improve spatial resolution. An important requirement for high resolution work arises from experiments on coherent X-ray beams, e.g. speckle interferometry [21] and photon correlation spectroscopy [22]. Coherent X-ray studies, made possible by the extreme brilliance of undulator sources on the third generation synchrotrons, present some of the most demanding needs for the detector systems now under development.

In summary, CCD-array technology offers interesting approaches to many of the detector needs for synchrotron radiation. It is quite clear that the unique capabilities of the new generation of sources that are coming into use over the next few years will not be fully exploited without appropriate detector development.

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