Semiconductor materials grown at low temperatures have recently attracted considerable attention. It has been demonstrated that low temperature GaAs (LT GaAs) grown by molecular beam epitaxy (MBE) in the temperature range of 200–300 °C is highly nonstoichiometric with a large, up to 1%, excess of As. In as-grown material the nonstoichiometry is accommodated by point defects: arsenic antisites, gallium vacancies, and possibly by very small arsenic clusters. Upon annealing at about 600 °C most of the excess arsenic diffuses to form extended As precipitates. The appearance of the precipitates has been associated with a change in electronic properties, most notably with a large increase of the resistivity and shortening of the carrier lifetime. It has also been shown that similar properties can be achieved in As implanted and annealed GaAs.

Although there is a consensus that precipitates and/or point defects play the key role in determining the properties of LT- and ion-implanted GaAs, the actual importance of different types of defects is still a controversial and hotly debated issue. A strong correlation between the presence of precipitates and the onset of desirable properties of LT- or ion-implanted GaAs has been established in numerous studies. Also, it has been reported that high resistivity GaAs can be produced by implantation of high doses of Ni and Fe which tend to precipitate upon high temperature annealing. The role of point defects is more difficult to elucidate, as they can be observed only indirectly and quite often their concentration is too low to be detected in thin implanted layers. Nevertheless, there is a body of evidence indicating that point defects are necessary if not sufficient for producing high resistivity, short carrier lifetime material.

However, in all the previous studies the material was either highly nonstoichiometric or had high concentrations of metallic impurities. In both instances metallic precipitates were formed during the annealing. In order to determine if the metallic precipitates are necessary to achieve a high resistivity, short carrier lifetime material, we have performed extensive investigations of the electrical and optical properties of stoichiometric GaAs implanted with inert argon gas ions. We expect this material to have a high concentration of damage induced native defects without metallic precipitates.

Semi-insulating (SI) and n-type GaAs wafers were implanted with Ar ions so that the depth of the damaged layer, as measured by Rutherford backscattering, was about 100 nm. Samples were annealed for 30 s at temperatures between 300 and 950 °C using rapid thermal annealing (RTA). Room temperature Hall effect was measured on the implanted SI substrates and the n-type wafers were used for magnetic circular dichroism absorption (MCDA) measurements. Also low temperature sub-bandgap absorption was measured on the as-implanted and annealed samples.

In order to isolate the optical response of the implanted layer from that of the bulk the carrier lifetime measurements were performed on a MBE layer removed from its native substrate by epitaxial liftoff. The 400-nm-thick MBE layer was grown at 600 °C. A proper implantation schedule with four different Ar ion doses and energies was employed to achieve uniform damage and full amorphization throughout the layer. Selected samples were studied by transmission electron microscopy (TEM) to reveal implantation induced extended defects.

Figures 1(a) and 1(b) show results of the Hall effect measurements for the samples implanted with Ar ions of energy 120 keV and a dose of $1.6\times10^{16}$ cm$^{-2}$. A relatively low sheet resistance and very low mobility in the as-implanted material can be attributed to a hopping conductivity via defects sites. The resistivity was found to increase with increasing annealing temperature. It reaches a maximum for an annealing temperature of about 600–700 °C. A very significant increase in the electron mobility is also observed after annealing in this temperature range. The highest mobility of about 2000 cm$^2$/V s was found in a sample annealed at 700 °C. At even higher annealing temperatures the mobility decreases again. A similar effect was previously observed by others and explained by a compensation with carbon acceptors adsorbed on chemically cleaned surfaces and incorporated.

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The Hall effect results are consistent with infrared absorption measurements which show a very large reduction of the subband gap absorption upon annealing at 600 °C indicating a large decrease in the concentration of neutral AsGa\(_{\text{As}}\)\(_{\text{As}}\). MCDA measurements were performed on n-type substrates implanted with the same Ar ion energy and dose. The MCDA signal can be calibrated to provide information on the concentration of singly ionized arsenic antisites. As shown in Fig. 2 an increase of the concentration AsGa\(^{+}\) is observed for an annealing temperature \(T_A\) starting somewhere between 350 and 400 °C, and the maximum of \(7\times10^{19}\) cm\(^{-3}\) AsGa\(^{+}\) was found for \(T_A=500\) °C. It is interesting to note that the maximum concentration of AsGa\(^{+}\) corresponds to the annealing temperature where a rapid increase in the resistivity and mobility is found. To further elucidate the properties of the Ar implanted GaAs we have measured the carrier lifetime. The measurements were performed on a \(\approx 500\)-nm-thick MBE grown GaAs layer implanted with Ar ions and lifted off the GaAs substrate. A 900 nm AlGaAs buffer layer was used as an etch stop layer. In order to achieve a uniform damage profile the GaAs layer was consecutively implanted with Ar ions with energies of 275, 125, 80, 50 keV and doses of \(3\times10^{15}\), \(9\times10^{14}\), \(1\times10^{14}\), \(4\times10^{14}\) cm\(^{-2}\), respectively. This resulted in a uniform distribution of implanted ions with a bulk concentration of about \(1.6\times10^{20}\) cm\(^{-3}\). The material was annealed at 600 °C, i.e., at the temperature where a maximum in electrical resistivity was observed. A bulk resistivity of the order of \(10^5\ \Omega\ \text{cm}\) was measured directly on the lifted off film. Figure 3 shows normalized output voltage pulses obtained after applying several different bias voltages on a simple photoconductive detector fabricated on the GaAs layer. The 20-\(\mu\)m-wide photoconductive gap was illuminated by a 100 fs laser pulse and the measurement was made using an external electro-optic sampling probe. The fast fall time indicates that the excited carriers are trapped within less than 1 ps. The return of the signal to the baseline, without any noticeable tail response, demonstrates that the trapping of photoexcited carriers is not only rapid but apparently also complete. The negative tail and the small bumps trailing the pulse are due to the response of the electro-optic probe and/or time domain reflections from the surrounding transmission line. The bias level of the photoconductor was varied in order to substantiate that there was no change in the signal versus electric field due to Schottky contacts or field screening. A similar reduction of the carrier trapping time was previously observed in proton implanted GaAs.\(^{16}\) It should be noted that one aspect of the ion implanted material that could be brought into questions, i.e., its reproducibility, was investigated through lifetime measurements on two different samples implanted at different times under identical conditions and annealed at 600 °C. Results obtained using pump-probe femtosecond spectroscopy demonstrate that the...
same, approximately 400 fs, trapping time is observed for both sets of samples.

The present study clearly shows that Ar implanted GaAs has all the essential optical and electronic properties of As implanted or LT grown GaAs. However, since in this case there is no excess As, we do not expect to have As precipitates. To confirm this presumption we have performed a TEM study of Ar implanted GaAs. We find that the as implanted sample has a 52-nm-thick amorphous layer at the sample surface and the end of the range defects at about 100–160 nm below the surface. Small dots are the artifacts caused by the ion milling. As illustrated in Fig. 4(a), annealing at an even higher temperature of 950 °C leads to a coarsening of the voids and an increase of their diameter to 10 nm. Although it is not obvious at the moment what role such voids play in determining the properties of this material, it is quite clear that unlike metallic precipitates the voids are not expected to accumulate a large excess charge and to form depleted regions.

In conclusion, our study shows that Ar implanted and annealed GaAs exhibit properties similar to those previously reported in As rich GaAs. We find that a high resistivity, high mobility, and short lifetime material can be obtained without metallic precipitates.

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